

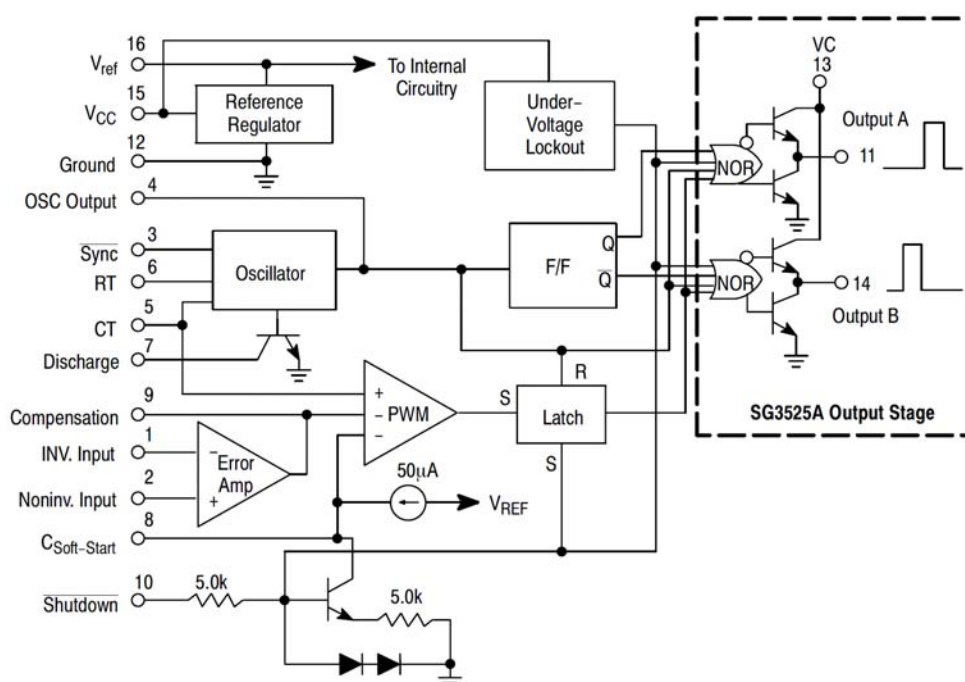
description

The accuracy of the chip +5.1V reference voltage is 1%. Because the reference voltage is within the input common-mode range of the error amplifier, there is no need for an external resistor. 3525 can work in masterslave mode and can also be synchronized with external clock. The dead time can be adjusted by the resistance between CT and discharge terminal. Other functional circuits inside the chip also include: soft start circuit, turnoff circuit and under- voltage circuit.

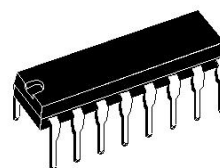
3525 The output stage of the control chip is a high- power totem- pole output, and its source current and sink current exceed 150mA, which gives a logic level of "NOR" and a low level when it is in the "off" state. trait

- The working voltage range is 8~35V;
- 5.1V reference voltage with accuracy of 1%;
- The oscillation frequency range is 100Hz~500KHz;
- The oscillator synchronization signal input end;
- Dead time is adjustable;
- Built-in soft start circuit;
- Step- by- step pulse shutdown;
- Input undervoltage locking with hysteresis voltage;
- PWM locking function, prohibiting multi- pulse;

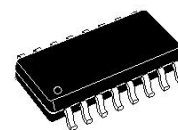
Internal block diagram



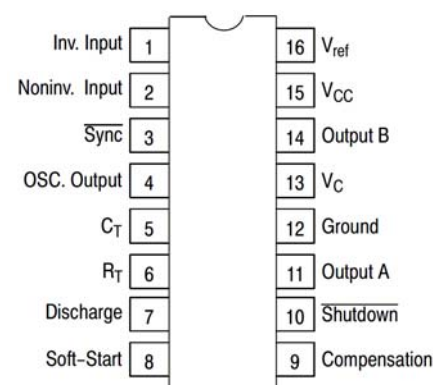
Package



DIP16



SOP16



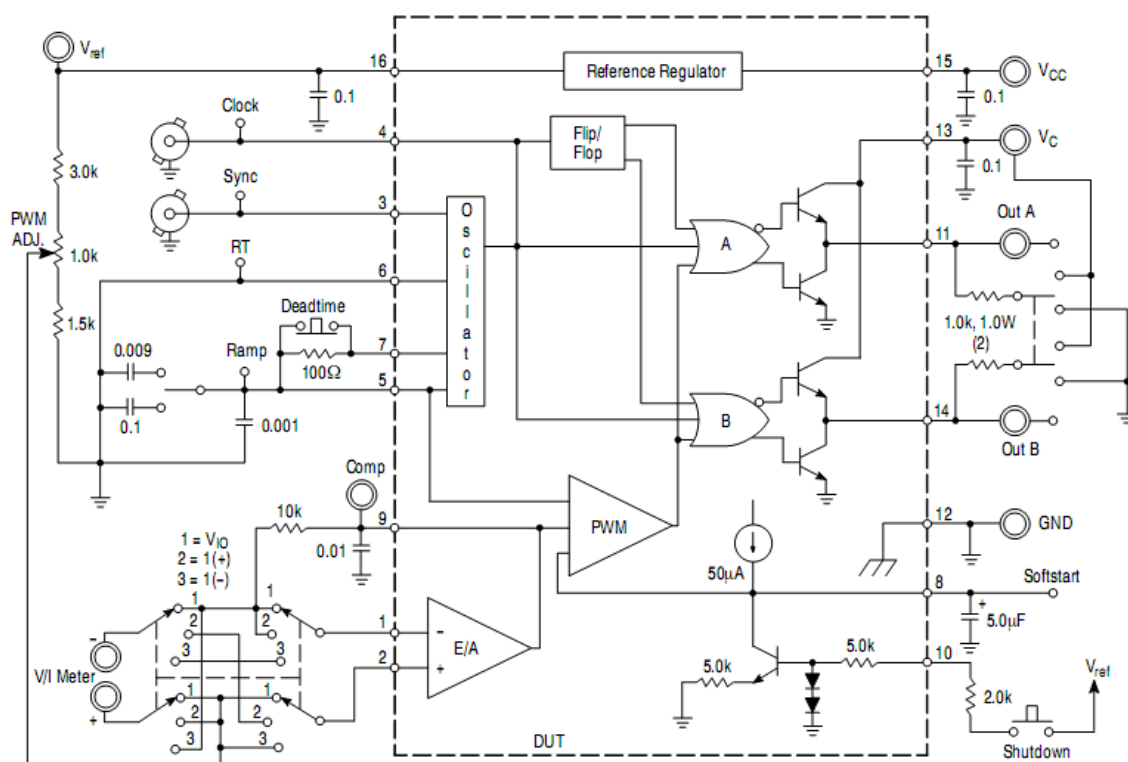
Limit operating parameters

symbol	parameter	value	unit
V_i	Power supply voltage	40	V
V_C	Collector supply voltage	40	V
IOSC	Oscillator charging current	5	mA
I_o	Output source current,	400	mA
IR	Reference output current	50	mA
Ptot	Power consumption Tamb= 70°C	1000	mW
Top	Working temperature	0 to 70	°C
Tj	Junction temperature range	– 55 to 150	°C
Tstg	Storage temperature range	– 65 to 150	°C

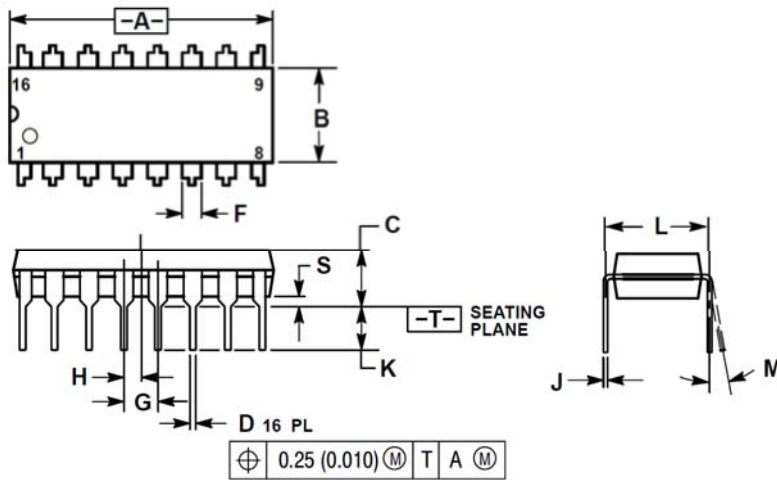
Electrical parameters (Vcc=20V)

parameter	symbol	condition	minimum value	typical value	maximum	unit
Reference source						
Reference output voltage	VREF	TJ = 25°C	5.0	5.1	5.2	V
Line rule	ΔV_{REF}	VCC = 8 to 35V	–	9	20	mV
Load regulation	ΔV_{REF}	IREF = 0 to 20mA	–	20	50	mV
Short circuit output current	ISC	VREF = 0, TJ = 25°C	–	80	100	mA
Total output change	ΔV_{REF}	Line, load and temperature	4.95	–	5.25	V
temperature stability	STT		–	20	50	mV
long term stability	ST	TJ = 125°C, 1 KHRS	–	20	50	mV
Oscillating part						
Initial accuracy	ACCUR	TJ = 25°C	–	3	6	%
Frequency varies with voltage	$\Delta f / \Delta V_{CC}$	VCC = 8 to 35V	–	0.8	2	%
highest frequency	f (MAX)	RT = 2K Ω , CT = 470pF	400	430	–	KHz
Lowest frequency	f (MIN)	RT = 200K Ω , CT = 0.1 μ F	–	60	120	Hz
Clock amplitude	V (CLK)		3	4	–	V
Clock width	tW (CLK)	TJ = 25°C	0.3	0.6	1	us
Synchronization threshold	VTH (SYNC)		1.2	2	2.8	V
Synchronous input current	II (SYNC)	Sync = 3.5V	–	1.3	2.5	mA
Error amplifier section (VCM = 5.1V)						
Input offset voltage	VIO		–	1.5	10	mV
Input bias current	I BIAS		–	1	10	μ A
Input offset current	I IO		–	0.1	1	μ A
Open loop voltage gain	GVO	RL $\geq$ 10M Ω	60	80	–	dB
common mode rejection ratio	CMRR	VCM = 1.5 to 5.2V	60	90	–	dB
Power supply rejectionratio	PSRR	VCC = 8 to 3.5V	50	60	–	dB

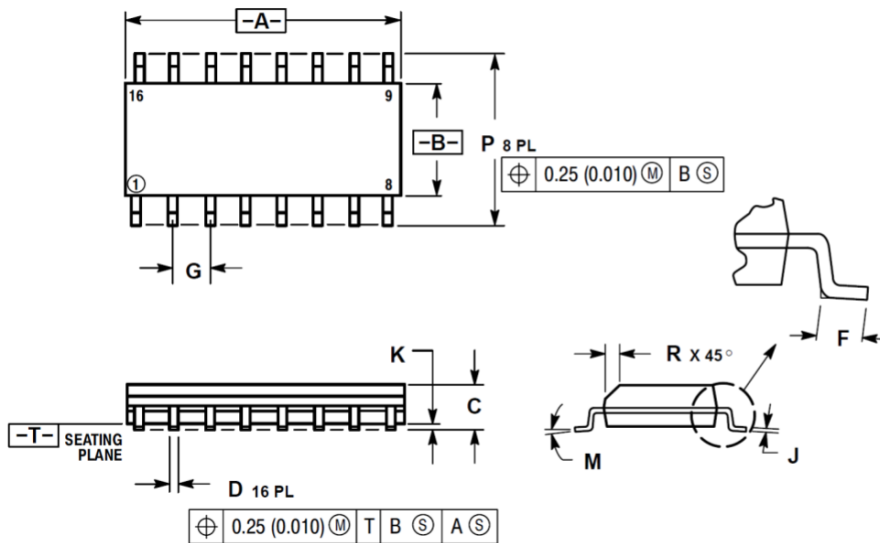
Test line



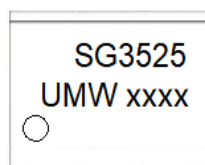
DIP16



SOP16



Marking



Ordering information

Order code	Package	Baseqty	Deliverymode
UMW SG3525AP	SOP-16	2500	Tape and reel
UMW SG3525AN	DIP -16	2500	Tape and reel